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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/500,507	06/29/2004	Peter Lurkens	DE 020010	4384

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PHILIPS INTELLECTUAL PROPERTY & STANDARDS
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EXAMINER

VY, HUNG T

ART UNIT	PAPER NUMBER
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2821

DATE MAILED: 08/09/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<p align="center">Office Action Summary</p>	Application No. 10/500,507	Applicant(s) LURKENS, PETER	
	Examiner Hung T. Vy	Art Unit 2821	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 6/29/04
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-11 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-11 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>6/9/04 & 11/26/04</u> | 6) <input type="checkbox"/> Other: _____ |

6/29/04

DETAILED ACTION

Priority Claim Acknowledgement

1. Receipt is acknowledged of papers submitted under 35 USC 119(a)-(d), which papers have been placed of record in the file.

Information Disclosure Statement

2. The information disclosure statement (IDS) submitted on June 29th, 2004 and November 26th, 2004 are in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

Claim Rejections - 35 U.S.C. § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 1 and 8-11 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Jungreis et al., U.S. patent No. 6,535,403.

With respect to claims 1 and 8-9, Jungreis et al. discloses a method of operating a high-pressure lamp and an electronic circuit for operating the load in an ignition mode and a normal operational mode, comprising a DC-AC converter comprising a first and a second half bridge Q₁, Q₂, Q₃, Q₄ which are connected in parallel between an operating potential (+) and a reference potential (-) for providing a suitable alternating current to the load in the two said operating

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modes; and a series arrangement comprising a first coil L1, followed by the Load, again followed by a second coil L2, while the connection terminal of the first coil L1 not connected to the Load is connected to the output Q₁, Q₂ of the first half bridge Q₁, Q₂, and the connection terminal of the second coil L2 not connected to the Load is connected to the output Q₃, Q₄ of the second half bridge Q₃, Q₄, said outputs being each formed by a central tap of a half bridge; characterized by a first capacitor C1 which is connected in the path from the junction point of the first coil L1 and the Load either to the reference potential (-) or to the operating potential (+); and a second capacitor C2 which is connected in the path from the junction point of the Load and the second coil L2 either to the reference potential (-) or to the operating potential (+) or in parallel to the Load, Jungreis et al. does not explicitly teach that the load is a lamp or includes a lamp. However, the lamp has been commonly used as a load or included in a load (see prior art of record to Moisin, U.S. Patent No. 6,157,142; col. 7, lines 20-21). Therefore, to employ a lamp as the load or to include a lamp in the load of the electronic ballast circuit of Jungreis et al. for lighting purpose upon a particular application or environment of use would have been deemed obvious to an artisan skilled in the art of lamp power electronics.

With respect to claim 10, Jungreis et al. discloses with the same structure as claim invention so the system will provide the same function as invention.

With respect to claim 11, Jungreis et al. discloses the claimed invention except for voltageless switching. It would have been obvious to one having

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ordinary skill in the art at the time the invention was made to different kind of switching, since it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice. In re Leshin, 125 USPQ 416.

5. Claims 2-4 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Jungreis et al., U.S. patent No. 6,535,403 in view of Haas et al., U.S. Patent No. 5,712,536.

With respect to claims 2-4, Jungreis et al. discloses all of the claimed as expressly recited in claim 1, except for the third capacitor being connected between the output of the half bridge and either operating potential (+) or reference potential (-) and in that a fourth capacitor is connected between the operating potential (+) and the output of the first half bridge, Haas et al. discloses the third capacitor C_{s2} and fourth capacitor C_{s1} is connected between the output of the half bridge S_1, S_2 and either operating potential (+) or reference potential (-) (See fig. 3). It would have been obvious at the time the invention was made to a person having ordinary skill in the art to modify Jungreis et al. to have the third capacitor as taught by Haas et al. The motivation for doing so would have been to provide third capacitor and fourth capacitor is connected between the output of the half bridge and either operating potential (+) or reference potential (-) in order to have constitutes the voltage on boost third capacitor.

6. Claims 5 and 7 are rejected under 35 U.S.C. 103 (a) as being unpatentable over Jungreis et al., U.S. patent No. 6,535,403 in view of Pogadaev et al., U.S. Patent No. 6,369,526.

With respect to claim 5, Jungreis et al. disclose all limitation of invention except for the current sensor and comparator device. However, Pogadaev et al. disclose current sensor 8 and comparator device 6. It would have been obvious at the time the invention was made to a person having ordinary skill in the art to modify Jungreis et al. to by arranging the current sensor for generating a current sensor signal which represents the value of the current through the first coil and comparator device as taught by Haas et al. in order to effectively control the current supplied to the load or lamp.

With respect to claim 7, Pogadaev et al. disclose a delay device (See column 2, line 47-50)

7. Claims 6 is rejected under 35 U.S.C. 103 (a) as being unpatentable over Jungreis et al., U.S. patent No. 6,535,403 in view of Pogadaev et al., U.S. Patent No. 6,369,526 and further in view of Black, Jr. et al., U.S. Patent No. 5,831,426.

With respect to claim 6, Jungreis et al. and Pogadaev et al discloses all of the claimed limitations as expressly recited in claim 5 except for a magnetoresistive sensor. However, Black, Jr. et al discloses the magnetoresistive sensor as current sensor (See column 2, line 32). It would have been obvious at the time the invention was made to a person having ordinary skill in the art to modify Jungreis et al. and Pogadaev et al. to have current sensor as magnetoresistive sensor as taught by Black, Jr. et al. in order to provide more accurate representations of the input currents. Further more, It would have been obvious to one having ordinary skill in the art at the time the invention was made to different kind of current sensor, since it has been held to be within the general

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skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice. In re Leshin, 125 USPQ 416.

Conclusion

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hung Vy whose telephone number is (571) 272-1954. The examiner can normally be reached on Monday-Friday 8:30 am - 5:30pm. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on (571) 272-1834. The fax numbers for the organization where this application or proceeding is assigned are (703) 872-9306 for regular communications and (703) 308-7722 for After Final communications.

Information regarding the status of an application may be obtained from the patent Application Information Retrieval (PAIR) system. Status information for published application may be obtained from either private Pair or Public Pair. Status information for unpublished applications is available through Private Pair only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have question on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Hung T. Vy
Art Unit 2828
October 28, 2004



THUY V. TRAN
PRIMARY EXAMINER